Preferred Device

SWITCHMODE™ Power Rectifier

Designed for use in switching power supplies, inverters and as free wheeling diodes, these state-of-the-art devices have the following features:

- Ultrafast 60 Nanosecond Recovery Times
- 150°C Operating Junction Temperature
- Epoxy Meets UL94, V_O @ 1/8"
- High Temperature Glass Passivated Junction
- Low Leakage Specified @ 150°C Case Temperature
- Current Derating @ Both Case and Ambient Temperatures
- Electrically Isolated. No Isolation Hardware Required.
- UL Recognized File #E69369 (Note 1.)

Mechanical Characteristics

- Case: Epoxy, Molded
- Weight: 1.9 grams (approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Shipped 50 units per plastic tube
- Marking: U1660

MAXIMUM RATINGS

Please See the Table on the Following Page

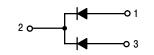
1. UL Recognized mounting method is per Figure 4.



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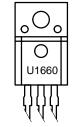
ULTRAFAST RECTIFIER 16 AMPERES 600 VOLTS





ISOLATED TO-220 CASE 221D STYLE 3

MARKING DIAGRAM



U1660 = Device Code

ORDERING INFORMATION

Device	Package	Shipping
MURF1660CT	TO-220	50 Units/Rail

Preferred devices are recommended choices for future use and best overall value.

MAXIMUM RATINGS (Per Leg)

Rating		Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage		V _{RRM} V _{RWM} V _R	600	Volts
Average Rectified Forward Current Total Device, (Rated V _R), T _C = 150°C	Per Diode Per Device	I _{F(AV)}	8 16	Amps
Peak Repetitive Forward Current (Rated V _R , Square Wave, 20 kHz), T _C = 150°C		I _{FM}	16	Amps
Non-repetitive Peak Surge Current (Surge applied at rated load conditions halfwave, single phase, 60 Hz)		I _{FSM}	100	Amps
Operating Junction and Storage Temperature		T _J , T _{stg}	- 65 to +150	°C
RMS Isolation Voltage (t = 1 second, R.H. \leq 30%, T _A = 25°C) (Note 3.) Per	Per Figure 3. Figure 4. (Note 2.) Per Figure 5.	V _{iso1} V _{iso2} V _{iso3}	4500 3500 1500	Volts

THERMAL CHARACTERISTICS (Per Leg)

Maximum Thermal Resistance, Junction to Case	$R_{\theta JC}$	3.0	°C/W
Lead Temperature for Soldering Purposes: 1/8" from Case for 5 Seconds	TL	260	°C

ELECTRICAL CHARACTERISTICS (Per Leg)

Characteristic	Symbol	Value	Unit
Maximum Instantaneous Forward Voltage (Note 4.)	٧ _F		Volts
(i _F = 8.0 Amp, T _C = 150°C)		1.20	
(i _F = 8.0 Amp, T _C = 25°C)		1.50	
Maximum Instantaneous Reverse Current (Note 4.)	i _R		μA
(Rated dc Voltage, $T_{C} = 150^{\circ}C$)		500	
(Rated dc Voltage, $T_C = 25^{\circ}C$)		10	
Maximum Reverse Recovery Time	t _{rr}		ns
(I _F = 1.0 Amp, di/dt = 50 Amp/μs)		60	
(I _F = 0.5 Amp, i _R = 1.0 Amp, I _{REC} = 0.25 Amp)		50	

2. UL Recognized mounting method is per Figure 4.

3. Proper strike and creepage distance must be provided.

4. Pulse Test: Pulse Width = 300 μ s, Duty Cycle \leq 2.0%.

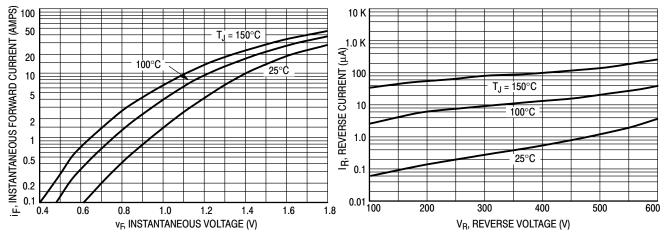


Figure 1. Typical Forward Voltage, Per Leg

Figure 2. Typical Reverse Current, Per Leg*

TEST CONDITIONS FOR ISOLATION TESTS*

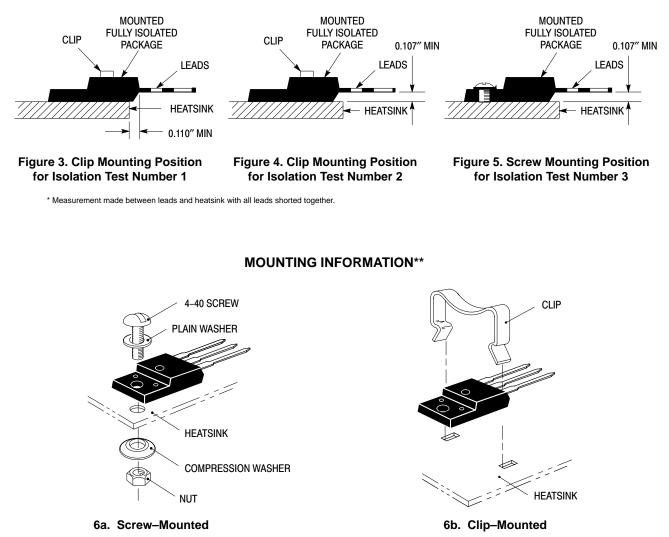


Figure 6. Typical Mounting Techniques

Laboratory tests on a limited number of samples indicate, when using the screw and compression washer mounting technique, a screw torque of 6 to 8 in \cdot lbs is sufficient to provide maximum power dissipation capability. The compression washer helps to maintain a constant pressure on the package over time and during large temperature excursions.

Destructive laboratory tests show that using a hex head 4–40 screw, without washers, and applying a torque in excess of 20 in \cdot lbs will cause the plastic to crack around the mounting hole, resulting in a loss of isolation capability.

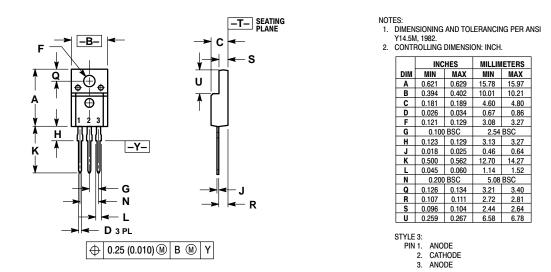
Additional tests on slotted 4–40 screws indicate that the screw slot fails between 15 to 20 in \cdot lbs without adversely affecting the package. However, in order to positively ensure the package integrity of the fully isolated device, ON Semiconductor does not recommend exceeding 10 in \cdot lbs of mounting torque under any mounting conditions.

**For more information about mounting power semiconductors see Application Note AN1040.

PACKAGE DIMENSIONS

TO-220 FULLPAK TRANSISTOR

CASE 221D-02 ISSUE D



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